



Attorney's Docket No.: 10559-487001/P11470/Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hooper, et al. Art Unit: 2664
Serial No.: 09/966,349 Examiner: Kevin D. Mew
Filed : September 28, 2001 Assignee: Intel Corporation
Title : MULTIPROTTOCOL DECAPSULATION/ENCAPSULATION CONTROL
STRUCTURE AND PACKET PROTOCOL CONVERSION METHOD

Mail Stop RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits of the Request for Continued Examination. No fee is required.

The documents cited on the Forms PTO-1449 are in the English language, with the exception of item noted below. Item JM (JP 59111533) is in the Japanese language and is provided with an English language abstract (item KG). Hence, in

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 17, 2006

Date of Deposit

Signature

Kelly M. Smith

Typed or Printed Name of Person Signing
Certificate

accordance with the requirements of 37 C.F.R. §1.98, as amended effective March 16, 1992, no further explanation of the listed items are necessary.

Applicant makes known to the Examiner the following U.S. applications, which are commonly owned and/or has one or more inventors in common:

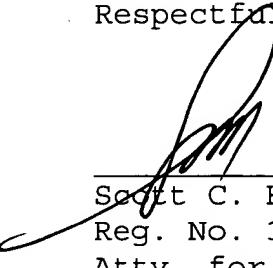
U.S.S.N.	Filing Date	Docket No.
09/473,571	12/28/1999	10559-128001
09/475,614	12/30/1999	10559-137001

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 2/17/96



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Substitute Form PTO-1449 (Modified) FEB 21 2006 (37 CFR §1.98(b))		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-487001	Application No. 09/966,349
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Donald F. Hooper, et al.		
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U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	3,373,408	3/12/1968	Ling			
	AB	3,478,322	11/11/1969	Evans			
	AC	3,736,566	5/29/1973	Anderson et al.			
	AD	3,792,441	2/12/1974	Wymore et al.			
	AE	3,940,745	2/24/1976	Sajeva			
	AF	4,016,548	4/5/1977	Law, et al.			
	AG	4,032,899	6/28/1977	Jenny, et al.			
	AH	4,075,691	2/21/1978	Davis, et al.			
	AI	4,130,890	12/19/1978	Adam			
	AJ	4,400,770	8/23/1983	Chan et al.			
	AK	4,514,807	4/30/1985	Nogi			
	AL	4,523,272	6/11/1985	Fukunaga et al.			
	AM	4,709,347	11/24/1987	Kirk, David L.			
	AN	4,745,544	5/17/1988	Renner et al.			
	AO	4,788,640	11/29/1988	Hansen			
	AP	4,831,358	5/16/1989	Ferrio, et al.			
	AQ	4,866,664	9/12/1989	Burkhardt, Jr. et al.			
	AR	4,890,218	12/26/1989	Bram			
	AS	4,890,222	12/26/1989	Kirk, David L.			
	AT	4,991,112	2/5/1991	Callemyn			
	AU	5,115,507	5/19/1992	Callemyn			
	AV	5,140,685	8/18/1992	Sipple et al.			
	AW	5,142,683	8/25/1992	Burkhardt, Jr. et al.			
	AX	5,155,831	10/13/1992	Emma et al.			
	AY	5,155,854	10/13/1992	Flynn et al.			
	AZ	5,168,555	12/1/1992	Byers et al.			
	BA	5,173,897	12/22/1992	Schrodi et al.			

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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	BB	5,255,239	10/19/1993	Taborn et al.			
	BC	5,263,169	11/16/1993	Genusov et al.			
	BD	5,347,648	9/13/1994	Stamm et al.			
	BE	5,367,678	11/22/1994	Lee et al.			
	BF	5,390,329	2/14/1995	Gaertner et al.			
	BG	5,392,391	2/21/1995	Caulk, Jr. et al.			
	BH	5,392,411	2/21/1995	Ozaki			
	BI	5,392,412	2/21/1995	McKenna			
	BJ	5,404,482	4/4/1995	Stamm et al.			
	BK	5,404,484	4/4/1995	Bennett			
	BL	5,404,464	4/4/1995	Bennett			
	BM	5,404,469	4/4/1995	Chung			
	BN	5,432,918	7/11/1995	Stamm			
	BO	5,448,702	9/5/1995	Garcia, Jr. et al.			
	BP	5,450,351	9/12/1995	Heddes			
	BQ	5,452,437	9/19/1995	Richey et al.			
	BR	5,459,842	10/17/1995	Begun et al.			
	BS	5,459,843	10/17/1995	Davis			
	BT	5,463,625	10/31/1995	Yasrebi			
	BU	5,467,452	11/14/1995	Blum et al.			
	BV	5,485,455	1/16/1996	Dobbins et al.			
	BW	5,515,296	5/7/1996	Agarwal, Rohit			
	BX	5,517,648	5/14/1996	Bertone et al.			
	BY	5,542,070	7/30/1996	LeBlanc et al.			
	BZ	5,542,088	7/30/1996	Jennings, Jr. et al.			
	CA	5,544,236	8/6/1996	Andruska et al.			
	CB	5,550,816	8/27/1996	Hardwick et al.			

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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	CC	5,557,766	9/17/1996	Takiguchi et al.			
	CD	5,568,476	10/22/1996	Sherer, et al.			
	CE	5,568,617	10/22/1996	Kametani			
	CF	5,574,922	11/12/1996	James			
	CG	5,581,729	12/3/1996	Nistala et al.			
	CH	5,592,622	1/7/1997	Isfeld et al.			
	CI	5,613,071	3/18/1997	Rankin et al.			
	CJ	5,613,136	3/18/1997	Casavant et al.			
	CK	5,623,489	4/22/1997	Cotton et al.			
	CL	5,627,829	5/6/1997	Gleeson et al.			
	CM	5,630,130	5/13/1997	Perotto et al.			
	CN	5,630,641	5/20/1997	Perotto et al.			
	CO	5,644,623	7/1/1997	Gutledge			
	CP	5,649,157	7/15/1997	Williams			
	CQ	5,659,687	8/19/1997	Kim et al.			
	CR	5,680,641	10/21/1997	Sidman			
	CS	5,689,566	11/18/1997	Nguyen			
	CT	5,699,537	12/16/1997	Sharangpani et al.			
	CU	5,701,434	12/23/1997	Nakagawa			
	CV	5,717,898	2/10/1998	Kagan et al.			
	CW	5,721,870	2/24/1998	Matsumoto			
	CX	5,740,402	4/14/1998	Bratt, et al.			
	CY	5,742,587	4/21/1998	Zornig et al.			
	CZ	5,742,782	4/21/1998	Ito et al.			
	DA	5,742,822	4/21/1998	Motomura			
	DB	5,745,913	4/28/1998	Pattin et al.			
	DC	5,751,987	5/12/1998	Mahant-Shetti et al.			

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	DD	5,761,507	6/2/1998	Govett			
	DE	5,761,522	6/2/1998	Hisanga et al.			
	DF	5,764,915	6/9/1998	Heimsoth, et al.			
	DG	5,768,528	6/16/1998	Stumm			
	DH	5,781,551	7/14/1998	Born			
	DI	5,781,774	7/14/1998	Krick			
	DJ	5,784,649	7/21/1998	Begur			
	DK	5,784,712	7/21/1998	Byers et al.			
	DL	5,796,413	8/18/1998	Shipp et al.			
	DM	5,797,043	8/18/1998	Lewis et al.			
	DN	5,809,235	9/15/1998	Sharma et al.			
	DO	5,809,237	9/15/1998	Watts et al.			
	DP	5,809,530	9/15/1998	Samra et al.			
	DQ	5,812,868	9/22/1998	Moyer et al.			
	DR	5,828,746	10/27/1998	Ardon			
	DS	5,828,863	10/27/1998	Barrett et al.			
	DT	5,832,215	11/3/1998	Kato et al.			
	DU	5,835,755	11/10/1998	Stellwagen, Jr.			
	DV	5,850,530	12/15/1998	Chen, et al.			
	DW	5,854,922	12/29/1998	Gravenstein et al.			
	DX	5,860,158	1/12/1999	Pai et al.			
	DY	5,886,992	3/23/1999	Raatikainen et al.			
	DZ	5,887,134	3/23/1999	Ebrahim			
	EA	5,890,208	3/30/1999	Kwon			
	EB	5,892,979	4/6/1999	Shiraki et al.			
	EC	5,898,701	4/27/1999	Johnson, David L.			
	ED	5,905,876	5/18/1999	Pawlowski et al.			

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	EE	5,905,889	5/18/1999	Wilhelm, Jr.			
	EF	5,915,123	6/22/1999	Mirsky et al.			
	EG	5,918,235	6/29/1999	Kirshenbaum, et al.			
	EH	5,928,736	7/27/1999	Muller et al.			
	EI	5,933,627	8/3/1999	Parady et al.			
	EJ	5,937,187	8/10/1999	Kosche et al.			
	EK	5,938,736	8/17/1999	Muller et al.			
	EL	5,940,612	8/17/1999	Brady et al.			
	EM	5,940,866	8/17/1999	Chisholm et al.			
	EN	5,946,487	8/31/1999	Dangelo			
	EO	5,948,081	9/7/1999	Foster			
	EP	5,958,031	9/28/1999	Kime			
	EQ	5,961,628	10/5/1999	Nguyen et al.			
	ER	5,970,013	10/19/1999	Fischer et al.			
	ES	5,978,838	11/2/1999	Mohamed et al.			
	ET	5,983,274	11/9/1999	Hyder et al.			
	EU	5,995,513	11/30/1999	Harrand, et al.			
	EV	6,012,151	1/4/2000	Mano			
	EW	6,014,729	1/11/2000	Lannan et al.			
	EX	6,023,742	2/8/2000	Ebeling et al.			
	EY	6,032,190	2/29/2000	Bremer et al.			
	EZ	6,058,168	5/2/2000	Braband			
	FA	6,061,710	5/9/2000	Eickemeyer, et al.			
	FB	6,067,300	5/23/2000	Baumert et al.			
	FC	6,067,585	5/23/2000	Hoang			
	FD	6,070,231	5/30/2000	Ottinger			
	FE	6,072,781	6/6/2000	Feeney et al.			

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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	FF	6,073,215	6/6/2000	Snyder			
	FG	6,079,008	6/20/2000	Clery, III			
	FH	6,085,215	7/4/2000	Ramakrishnan et al.			
	FI	6,085,294	7/4/2000	Van Doren et al.			
	FJ	6,092,127	7/18/2000	Tausheck			
	FK	6,092,158	7/18/2000	Harriman et al.			
	FL	6,111,886	8/29/2000	Stewart			
	FM	6,112,016	8/29/2000	MacWilliams et al.			
	FN	6,134,665	10/17/2000	Klein et al.			
	FO	6,141,677	10/31/2000	Hanif et al.			
	FP	6,141,689	10/31/2000	Yasrebi			
	FQ	6,141,765	10/31/2000	Sherman			
	FR	6,144,669	11/7/2000	Williams et al.			
	FS	6,145,054	11/7/2000	Mehrotra et al.			
	FT	6,157,955	12/5/2000	Narad et al.			
	FU	6,160,562	12/12/2000	Chin et al.			
	FV	6,170,051	1/2/2001	Dowling			
	FW	6,182,177	1/30/2001	Harriman			
	FX	6,195,676	2/27/2001	Spix et al.			
	FY	6,199,133	3/6/2001	Schnell			
	GA	6,201,807	3/13/2001	Prasanna			
	GB	6,212,542	4/3/2001	Kahle et al.			
	GC	6,212,611	4/3/2001	Nizar et al.			
	GD	6,216,220	4/10/2001	Hwang			
	GE	6,223,207	4/24/2001	Lucovsky et al.			
	GF	6,223,238	4/24/2001	Meyer et al.			
	GG	6,223,274	4/24/2001	Catthoor, et al.			

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	GH	6,223,279	4/24/2001	Nishimura et al.			
	GI	6,247,025	6/12/2001	Bacon			
	GJ	6,256,713	7/3/2001	Audityan et al.			
	GK	6,272,520	8/7/2001	Sharangpani et al.			
	GL	6,272,616	8/7/2001	Fernando et al.			
	GM	6,275,505	8/14/2001	O'Loughlin et al.			
	GN	6,279,113	8/21/2001	Vaidya			
	GO	6,282,169	8/28/2001	Kiremidjian			
	GP	6,286,083	9/4/2001	Chin, et al.			
	GQ	6,289,011	9/11/2001	Seo et al.			
	GR	6,295,600	9/25/2001	Parady et al.			
	GS	6,298,370	10/2/2001	Tang et al.			
	GT	6,307,789	10/23/2001	Wolrich et al.			
	GU	6,311,261	10/30/2001	Chamdani et al.			
	GV	6,324,624	11/27/2001	Wolrich et al.			
	GW	6,338,078	1/8/2002	Chang, et al.			
	GX	6,345,334	2/5/2002	Nakagawa et al.			
	GY	6,347,344	2/12/2002	Baker et al.			
	GZ	6,356,692	3/12/2002	Kasper et al.			
	HA	6,360,262	3/19/2002	Guenthner et al.			
	HB	6,366,998	4/2/2002	Mohamed			
	HC	6,373,848	4/16/2002	Allison et al.			
	HD	6,389,449	5/14/2002	Nermirovsky et al.			
	HE	6,393,026	5/21/2002	Irwin			
	HF	6,393,483	5/21/2002	Latif et al.			
	HG	6,415,338	7/2/2002	Habot			
	HH	6,424,659	7/23/2002	Viswanadham et al.			

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	HI	6,426,940	7/30/2002	Seo et al.			
	HJ	6,427,196	7/30/2002	Adiletta et al.			
	HK	6,430,626	8/6/2002	Witkowski et al.			
	HL	6,434,145	8/13/2002	Opsasnick et al.			
	HM	6,453,404	9/17/2002	Bereznyi, et al.			
	HN	6,463,072	10/8/2002	Wolrich et al.			
	HO	6,463,480	10/8/2002	Kikuchi, et al.			
	HP	6,463,527	10/8/2002	Vishkin			
	HQ	6,466,898	10/15/2002	Chan			
	HR	6,477,562	11/5/2002	Nemirovsky et al.			
	HS	6,484,224	11/19/2002	Robins et al.			
	HT	6,526,451	2/25/2003	Kasper et al.			
	HU	6,529,983	3/4/2003	Marshall, et al.			
	HV	6,532,509	3/11/2003	Wolrich et al.			
	HW	6,535,878	3/18/2003	Guedalia, et al.			
	HX	6,552,826	4/22/2003	Adler et al.			
	HY	6,553,406	4/22/2003	Berger et al.			
	HZ	6,560,667	5/6/2003	Wolrich et al.			
	IA	6,570,850	5/27/2003	Gutierrez et al.			
	IB	6,577,542	6/10/2003	Wolrich et al.			
	IC	6,584,522	6/24/2003	Wolrich et al.			
	ID	6,587,906	7/1/2003	Wolrich et al.			
	IE	6,604,125	8/5/2003	Belkin			
	IF	6,606,704	8/12/2003	Adiletta et al.			
	IG	6,625,654	9/23/2003	Wolrich et al.			
	IH	6,629,236	9/30/2003	Aipperspach et al.			
	II	6,631,422	10/7/2003	Althaus et al.			

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	IJ	6,631,430	10/7/2003	Wolrich et al.			
	IK	6,631,462	10/7/2003	Wolrich et al.			
	IL	6,661,794	12/9/2003	Wolrich et al.			
	IM	6,667,920	12/23/2003	Wolrich et al.			
	IN	6,668,317	12/23/2003	Bernstein et al.			
	IO	6,671,827	12/30/2003	Guilford, et al.			
	IP	6,675,190	1/6/2004	Schabernack et al.			
	IQ	6,678,746	1/13/2004	Russell et al.			
	IR	6,681,300	1/20/2004	Wolrich et al.			
	IS	6,694,380	2/17/2004	Wolrich et al.			
	IT	6,792,488	9/14/2004	Wolrich, et al.			
	IU	6,876,561	4/5/2005	Wolrich et al.			
	IV	6,895,457	5/17/2005	Wolrich et al.			
	IW	6,925,637	8/2/2005	Thomas et al.			
	IX	6,952,824	10/4/2005	Hooper et al.			
	IY	6,976,095	12/13/2005	Wolrich et al.			
	IZ	6,983,350	1/3/2006	Adiletta et al.			
	JA	2004/0039895	2/26/2004	Wolrich et al.			
	JB	2004/0054880	3/18/2004	Bernstein et al.			
	JC	2004/0071152	4/15/2004	Wolrich et al.			
	JD	2004/0073728	4/15/2004	Wolrich et al.			
	JE	2004/0073778	4/15/2004	Adiletta et al.			
	JF	2004/0098496	5/20/2004	Wolrich et al.			
	JG	2004/0109369	6/10/2004	Wolrich et al.			

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Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-487001	Application No. 09/966,349
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Donald F. Hooper, et al.		
		Filing Date September 28, 2001	Group Art Unit 2664	

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	JH	EP 0 379 709	8/1/1990	Europe			
	JI	EP 0 464 715	1/8/1992	Europe			
	JJ	EP 0 633 678	1/11/1995	Europe			
	JK	EP 0 745 933	12/4/1996	Europe			
	JL	EP 0 809 180	11/26/1997	Europe			
	JM	JP 59111533	6/27/1984	Japan			X*
	JN	WO 01/16718	3/8/2001	WIPO			
	JO	WO 01/16769	3/8/2001	WIPO			
	JP	WO 01/16770	3/8/2001	WIPO			
	JQ	WO 01/16782	3/8/2001	WIPO			
	JR	WO 01/48596	7/5/2001	WIPO			
	JS	WO 01/48606	7/5/2001	WIPO			
	JT	WO 01/48619	7/5/2001	WIPO			
	JU	WO 01/50247	7/12/2001	WIPO			
	JV	WO 01/50679	7/12/2001	WIPO			
	JW	WO 03/030461	4/10/2003	WIPO			
	JX	WO 94/15287	7/7/1994	WIPO			
	JY	WO 97/38372	10/16/1997	WIPO			

X* = An English language abstract is provided.

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	JZ	"10-/100-Mbps Ethernet Media Access Controller (MAC) Core"; NEC Electronics Inc., pp. 1-5, (December 1998).
	KA	Beckerle, M.J., "Overview of the START (*T) multithreaded computer" Compcon Spring '93, Digest of Papers, 22-26 February 1993, San Francisco, California, pp. 148-156 (1993), (abstract only)
	KB	Byrd et al., "Multithread Processor Architectures," IEEE Spectrum, 32(8): 38-46, (August 1995).
	KC	Chandranmenon, G.P. et al., "Trading Packet Headers for Packet Processing," IEEE/ACM Transactions on Networking 4(2): 141-152 (April 1996)
	KD	Dictionary of Computer Words: An A to Z Guide to Today's Computers, Revised Edition, Houghton Mifflin Company: Boston, Massachusetts, pp. 220, (1995)

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Other Documents (include Author, Title, Date, and Place of Publication)			
Examiner Initial	Desig. ID	Document	
	KE	Digital Semiconductor 21140A PCI Fast Ethernet LAN Controller, Hardware Reference Manual, Digital Equipment Corporation, pages i-x, 1-1 through 1-5, 2-1 through 2-12, 3-1 through 3-38, 4-31 through 5-2, 6-1 through 6-24, (March 1998)	
	KF	Doyle et al. (Eds.), <i>Microsoft Press Computer Dictionary</i> , 2 nd ed., Microsoft Press: Redmond: Washington, USA, pp. 326, (1994)	
	KG	English language abstract of Japanese Patent Application JP59111533, published 06/27/84, entitled: "Digital Data Arithmetic Circuit".	
	KH	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, pp. 146-156, (1995)	
	KI	Frazier, H., "Gigabit Ethernet: From 100 to 1,000 Mbps", IEEE Internet Computing, pp. 24-31, (January/February 1999).	
	KJ	Frazier, H., "The 802.3z Gigabit Ethernet Standard", IEEE Network, pp. 6-7, (May/June 1998)	
	KK	Gomez et al., "Efficient Multithreaded User-Space Transport for Network Computing: Design and Test of the TRAP Protocol," Journal of Parallel and Distributed Computing, 40(1):103-117, (1997).	
	KL	Haug et al., "Reconfigurable hardware as shared resource for parallel threads," IEEE Symposium on FPGAs for Custom Computing Machines, 2 pages, (1998).	
	KM	Hauser et al., "Garp: a MIPS processor with a reconfigurable coprocessor," Proceedings of the 5 th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, pp. 12-21, (1997).	
	KN	Hyde, R.L., "Overview of Memory Management," Byte, 13(4):219-225, (1998)	
	KO	Ippoliti, et al., "Parallel Media Access Controller for Packet Communications at Gb/s Rates", ICC/Supercom'90, Atlanta, Georgia, April 15, 1990, IEEE, pp. 991-996 (1990)	
	KP	Jenks, G. and J.L. Gaudiot, "Nomadic Threads: A migrating multithread approach to remote memory accesses in multiprocessors", Proceedings of the 1996 Conference on Parallel Architecture and Compilation Techniques, 20-23 October 1996, Boston, Massachusetts, (abstract only).	
	KQ	Litch, T. and J. Slaton, "StrongARMing Portable Communications," IEEE Micro, pp. 48-55 (March/April 1998)	
	KR	News.Com, August 26, 1999. "Enterprise Hardware, Intel Expected to Unveil New Networking Chip," http://new.com.com/Intel+expected+to+unveil+new+networking+chip/2100-1001_3-230315.html (accessed on 08/23/05), pages 1-5	
	KS	Schmidt et al., "The Performance of Alternative Threading Architectures for Parallel Communication Subsystems," http://www.cs.wustl.edu/~schmidt/PDF/JPDC-96.pdf#search='The%20Performance%20of%20Alternative%20Threading%20Architectures%20for%20Parallel%20Communication%20Subsystems' 13 November 1998., pp. 1-19	
	KT	Thistle et al., "A Processor Architecture for Horizon," Proceedings Supercomputing '88, IEEE Computer Society, 14-18 November 1988, Orlando, Florida, pp. 35-41 (1988)	
	KU	Tremblay et al., "A Three Dimensional Register File for Superscalar Processors," IEEE Proceedings of the 28 th Annual Hawaii International Conference on System Sciences, pp. 191-201 (1995)	
	KV	Trimberger et al, "A time-multiplexed FPGA," Proceedings of the 5 th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, pp. 22-28, (1998)	
	KW	Turner et al., "Design of a High Performance Active Router," Washington University in Saint Louis, Applied Research Lab, Online Internet Document, 18 March 1999 (20 pages)	

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Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	KX	Vibhatavanijt et al., "Simultaneous Multithreading-Based Routers," Proceedings of the 2000 International Conference of Parallel Processing, Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-369.
	KY	Wazlowski et al., "PRISM-II computer and architecture," IEEE Workshop on FPGAs for Custom Computing Machines, pp. 9-16, (1993)

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